

LP 5720



Page 1

Absolutely halide free lead-free solder paste

Description

LP 5720 is a no-clean, absolutely halide free and lead-free solder paste developed for increased stability on the stencil and clear residues after reflow.

The solder paste keeps its rheological properties under a wide variety of atmospheric conditions, assuring a stable and reproductive printing process.

LP 5720 is suitable for both low and high as well as short and long lead-free reflow profiles.

The residues of the solder paste are minimal and clear, also after long and high reflow profiles.

Residues are pin testable.

LP 5720 exhibits acceptable voiding for lead-free alloys.

LP 5720 is absolutely halide free providing optimal reliability after soldering.

The solder paste is classified as RO LO according IPC and EN standards.



Products pictured may differ from the product delivered

RoHS

Key properties

- High stability / High stencil life
- Wide process window in reflow
- Clear and minimal residue
- Absolutely halogen free

Availability

alloy	metal content	powder size	packaging
Sn96,5Ag3Cu0,5			
Sn99Ag0,3Cu0,7	printing:	type 3	jars :250g/500g
	~ 88.5%	type 4	<u>cartridges:</u>
Sn99,3Cu0,7			60z: 500g/600g/700g
		type 5 for certain alloys	12Oz: 1kg/1,2kg/1,3kg/1,5kg
			syringes: 5CC/10CC/30CC
Other alloys upon request			other packaging upon request

S.A. INTERFLUX® ELECTRONICS N.V - Eddastraat 51 - BE-9042 Gent - Belgium tel.: +32 9251 49 59 - fax.: +32 9251.4970 www.interflux.com - Info@interflux.com

Technical Data LP 5720 Ver: 4.2 04-04-17





Technical data LP 5720



Page 2

Profile recommendations for LP 5720

LP 5720 has been designed for a large process window in reflow. The solder paste can cope with both long and high as well as short and low profiles.

In general a profile with limited soak is advised. Also ramp profiles and soak profiles are possible. Soak profiles may be used when temperature differences across a board, due to a high mix of components or large board sizes, need to be levelled out or when voids, if present, need to be decreased.

When soldering an assembly in a lead-free reflow soldering process, care must be taken not to overheat components especially when using air convection or IR ovens. It is very important to know the temperature limitations of the components used on the board. To get a good thermal mapping of the board it is advised to use thermocouples and a thermal measuring tool. Measure on small outline, big outline and temperature sensitive components. Measure on the board side near the conveyor chain, in the middle of the board and close to, or on heat sinks.

Preheat

To allow absorbed moisture in the components to evaporate slowly and avoid component cracking, keep a steady heating rate between 1-3°C/s until about 200°C. For that purpose try to avoid a hot air temperature setting in the first heating zone above 150°C.

Soak

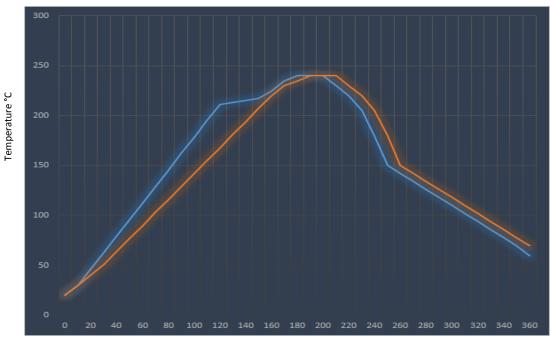
A soak zone between 200°C and 215°C can be used to level out temperature differences and/or reduce voiding.

Reflow

Peak temperature used is related to component specifications. In general between 235°C and 250°C. The time in liquidus (over melting point of the alloy used) could be between 45s and 90s.

Cooling

It is advisable to cool not faster than -4°C/s because of differences in thermal expansion of different materials (component and boards). Faster cooling in general gives stronger solder joints.



Time in seconds





Technical data LP 5720



Page 3

Handling

Storage

Store the solder paste in the original packaging, tightly sealed at a preferred temperature of 3° to 7°C. Shelf life is 6 months.

Handling

Let the solder paste reach room temperature prior to opening the packaging. Stir well before use.

Printing

Assure good sealing between PCB and stencil. A negative print gap of 0,2 to 0,4mm is advisable. Apply no more than enough squeegee pressure to get a clean stencil. Apply enough solder paste to the stencil to allow smooth rolling during printing. Regular replenish fresh solder paste.

Maintenance

Set an under stencil clean interval which provides continuous printing quality. **ISC8020** is recommended as cleaning agent in pre saturated wipes and USC liquid.

Reuse

Avoid mixing used and fresh paste in a jar. Do not put packages back into refrigeration when already opened. Store used paste in a separate jar at room temperature. A test board before reusing in production is advisable.

Test results conform IPC J-STD-004B/J-STD-005

Property	Result	Method
Chemical		
qualitative copper mirror	pass	J-STD-004B IPC-TM-650 2.3.32D
silver chromate (Cl, Br)	pass	J-STD-004B IPC-TM-650 2.3.33D
fluoride spot test	pass	J-STD-004B IPC-TM-650 2.3.35.1
corrosion test	pass	J-STD-004B IPC-TM-650 2.6.15
flux classification	RO LO	J-STD-004B
spread test	92,15 mm ²	J-STD-004B 2.4.46
Environmental SIR test	pass	J-STD-004B IPC-TM-650 2.6.3.7

Property		Method
after 15min	pass	J-STD-005 IPC-TM-650 2.4.43
after 4h	pass	J-STD-005 IPC-TM-650 2.4.43
	pass	J-STD-005 IPC-TM-650 2.4.45
after 15min at 25°C	pass	J-STD-005 IPC-TM-650 2.4.35
after 10min at 150°C	pass	J-STD-005 IPC-TM-650 2.4.35
	after 4h after 15min at 25°C	after 4h pass pass pass after 15min at 25°C pass



Ver: 4.2 04-04-17



Technical data LP 5720



Health and safety

Page 4

Please always consult the safety datasheet of the product.

Operating parameter recommendations

Printing

speed: 10—100 mm/sec squeegee pressure: 250g—350g/cm length U.S.C. interval: every 10 boards Preferred temperature range: 15 to 25°C Preferred humidity range: 40% to 75% r.H. Stencil life: >24hrs

Mounting

tack time: >8 hours

Reflow

reflow profile: linear and soak heating type: convection, ...

I.C.T

flying probe testable pin-bed testable

Cleaning

Cleaning of the paste from stencils and tools is recommended with Interflux $^{\otimes}$ ISC 8020.

The post reflow residues of LP 5720 are highly reliable and do not need to be cleaned, however they can be cleaned if desired.

Trade name: LP 5720 No-Clean, Halide Free, Lead-Free Solder Paste

Disclaimer

Because Interflux Electronics N.V. cannot anticipate or control the many different conditions under which this information and our products may be used, we do not guarantee the applicability or the accuracy of this information or the suitability of our products in any given situation. Users of our products should make their own test to determine the suitability of each such product for their particular purposes. The product discussed is sold without such warranty, either express or implied.

Copyright:

INTERFLUX ELECTRONICS N.V.

www.interflux.com



Ver: 4.2 04-04-17

Technical Data LP 5720